<i>i</i> -			H·1	
	Application No.	Applicant(s)		
Notice of Allowability	10/625,550	KIM ET AL.		
Notice of Allowability	Examiner	Art Unit		
	Ryan Lepisto	2883		
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.				
1. \boxtimes This communication is responsive to <u>amendment filed 25 Ju</u>	<u>uly 2005</u> .			
2. ☑ The allowed claim(s) is/are <u>1-15</u> .				
3. \boxtimes The drawings filed on <u>24 July 2003</u> are accepted by the Exa	aminer.			
4.				
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date	5. Notice of Informal P 6. Interview Summary Paper No./Mail Dat 8), 7. Examiner's Amendr 8. Examiner's Stateme 9. Other	(PTO-413), te ment/Comment	ŕ	

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DETAILED ACTION

Allowable Subject Matter

1. Claims 1-15 are allowed.

The following is an examiner's statement of reasons for allowance:

With regard to claims 1, 2, 4-5, 8 and 12: These claims are allowable over the prior art of record because the latter, either alone or in combination, does not disclose nor render obvious a process of forming opto-vias comprising drilling via holes in copper laminates, plating the inner walls of the holes, exposing and etching the upper and lower sides of the laminates to form circuit patterns, layering the laminates on each other using an insulating resin that fills the space between the laminates and via holes, removing the resin from the via holes and forming an optical waveguide or both electricvia or opto-via near the via holes or using epoxy resin with 95% or more light transmission or another method of forming vias comprising forming copper clad laminates with circuit patterns, layering them on top of each other, drilling electric and opto-vias holes, plating the holes, etching both sides of the laminates and drilling additional opto-vias in the order as disclosed in the claims or positioning a waveguide through each opto-via in a structure formed by stacking a plurality of copper clad laminates with circuit patterns and plated via holes using a resin adhesive and removing the resin to form vias, in combination with the rest of the claimed limitations.

With regard to claims 3, 6-7, 9-11 and 13-15: These claims are allowable over the prior art of record because they depend from allowable claims.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Magnuson et al (US 2002/0195716 A1) and Kang et al (US 2003/0169038 A1) are further pertinent to the state of the art.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ryan Lepisto whose telephone number is (571) 272-1946. The examiner can normally be reached on M-F 7:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Frank Font can be reached on (571) 272-2415. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Ryan Lepisto

Frank Font

Art Unit 2883

Supervisory Patent Examiner

Date: 8/23/05

Technology Center 2800